PA20-08-3PD(Z)3(6) Data Sheet

20 pin PLCC Auto-eject *Dead-Bug* socket/8 pin DIP 0.3" (0.6") plug

20 pin PLCC Lidded ZIF Live-Bug socket/8 pin DIP 0.3" (0.6") plug

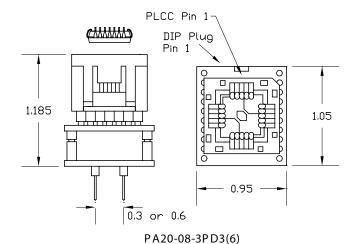
Supported Device/Footprints

This adapter allows several devices in the PLCC package to be programmed in their DIP footprint. Any devices that require this this 20 to 8 pin wiring are supported.

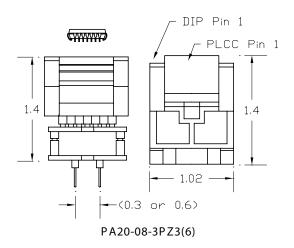
	Device	Footprint		
Mfgr	Device	Package	Device	Package
Several	Generic	PLCC	Generic	DIP

Adapter Dimensions

NOTE: THIS IS A DEAD-BUG SOCKET THE PLCC DEVICE IS INSERTED UPSIDE-DOWN



NOTE: THIS IS A LIVE-BUG SOCKET THE PLCC DEVICE IS INSERTED RIGHT-SIDE-UP



Adapter Part Numbers

The following chart identifies all the versions of this adapter currently available and the correct placement of the device to be programmed. For more information see the drawings at left.

Adapter	Plug	S ocket S tyle	Device Placement
PA20-08-3PD3	0.3"	Auto-eject	Pins up
PA20-08-3PD6	0.6"	Auto-eject	Pins up
PA20-08-3PZ3	0.3"	Lidded ZIF	Pins down
PA20-08-3PZ6	0.6"	Lidded ZIF	Pins down

Adapter Construction

The adapter is made up of 3 sub-assemblies. They assemble via connectors making the adapter modular. This way the sub-assemblies can be replaced when they wear out.

When disassembling the adapter take care not to bend the pins. When reassembling the adapter note the pin 1 indicators to align the parts correctly.

Test Socket

PLCC Auto-Eject test socket:

PLCC Lidded ZIF test socket:

20PLD(Z)

Accepts the test socket and connects the signals to the bottom board.

20-08-3-3(-6)

Accepts the top board and remaps the signals to a DIP footprint.

Adapter Wiring

The following chart shows the connections from the PLCC device to the adapter's DIP plug.

DEVICE	SIGNAL	PLUG	PLUG	SIGNAL	DEVICE
1	-	-	-	-	20
2	DATA	1	8	VCC	19
3	-	-	-	-	18
4	CLK	2	7	VPP	17
5	-	-	-	-	16
6	-	-	-	-	15
7	OE/RESET	3	6	CEO	14
8	CE	4	5	GND	13
9	-	-	-	-	12
10	-	-	-	-	11